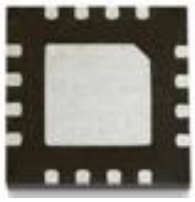
DATA SHEET

RELEASE Ø DATA SHEET





GRF0030

GaN HEMT Power Transistor
50 Watt P3dB
DC to 6 GHz

FEATURES

- Operating Frequency Range: DC to 6 GHz
- Operating Drain Voltage: 28 and 50 Volt
- Maximum Output Power: 50 Watt (P3dB)
- Suitable for Pulsed, Linear Applications
- 100% DC & RF Production Tested
- Compact 3 x 3 mm QFN-16 Package

Reference: 50 V / 50 mA / 3.5 GHz

- Saturated Gain: 12.7 dB
- Saturated Drain Efficiency: 60 %
- Saturated Output Power: 46.6 dBm

APPLICATIONS

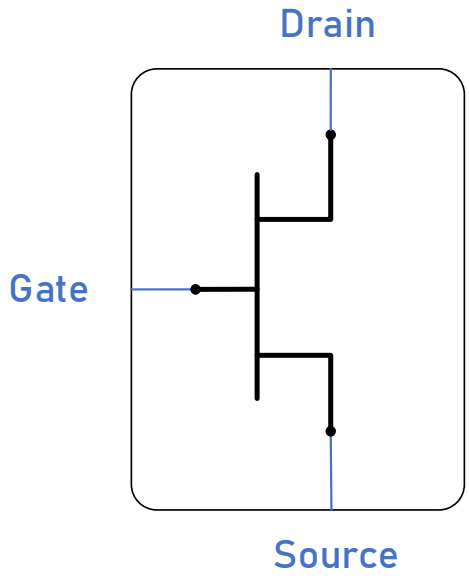
- Cellular Infrastructure
- Radar
- Communications
- Test Instrumentation

DESCRIPTION

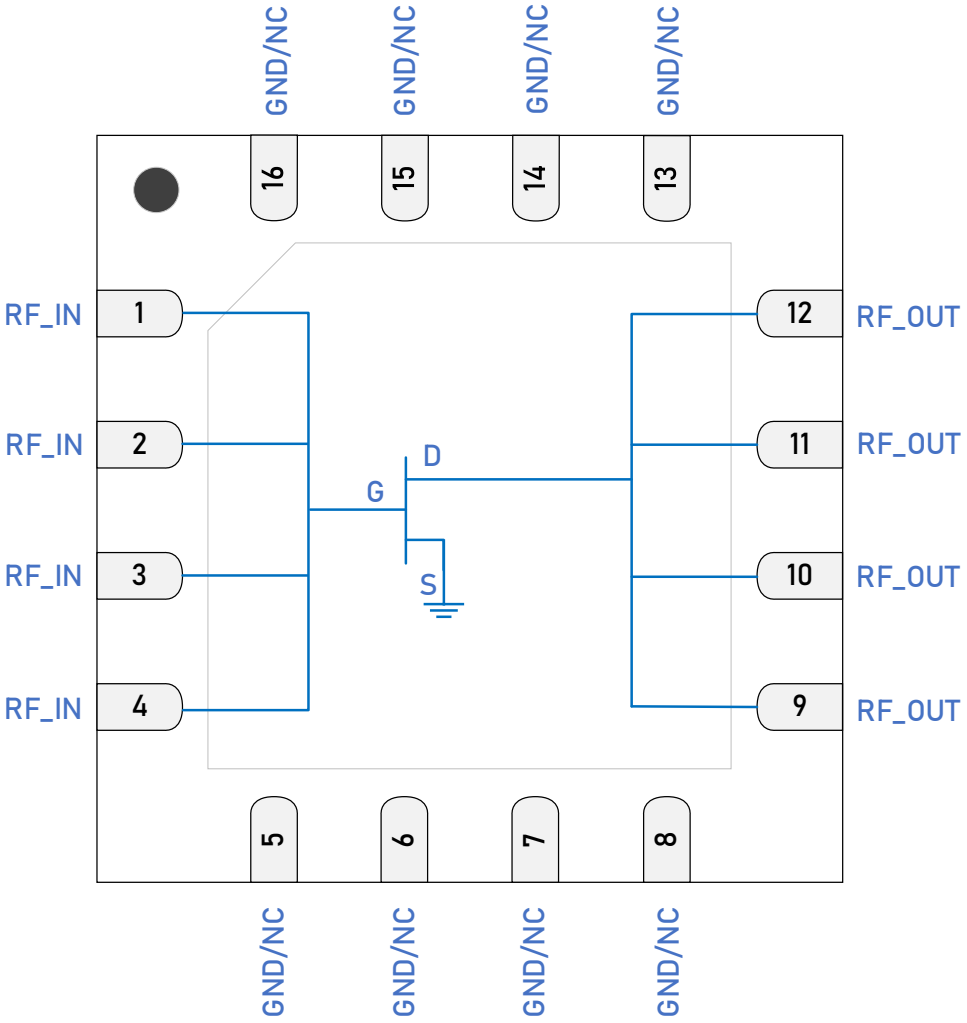
The GRF0030 is a 50 W (P3dB) unmatched discrete GaN-on-SiC HEMT which operates from DC to 6 GHz on a 50 volt supply rail. The wide bandwidth makes the GRF0030 suitable for a variety of applications including cellular infrastructure, radar, communications, and test instrumentation, and can support both linear and pulse operations. The device is housed in an industry-standard 3 x 3 mm surface mount QFN-16 package. Lead free and RoHS compliant.

Please consult with the GRF applications engineering team for custom tuning/evaluation board data.

BLOCK DIAGRAM



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3 x 3 mm Pin Out (Top View)

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Pin Assignments

Pin	Name	Description	Note
1, 2, 3, 4	RF_IN	Gate Voltage	Connected to GATE terminal of GaN transistor.
5, 6, 7, 8, 13, 14, 15, 16	GND/NC	Ground or No Connect	No internal connection to die. It is strongly recommended that these pins be connected to ground.
9, 10, 11, 12	RF_OUT	Drain Voltage	Connected to DRAIN terminal of GaN transistor.
PKG BASE	SOURCE	Source Connection	Connected to the SOURCE terminals of the GaN transistor. Provides DC and RF ground as well as thermal heat sink. **In order to match the device's rated performance, it is strongly recommended to use multiple thermal vias beneath the package for optimal RF and thermal performance**. Refer to evaluation board top layer graphic on schematic page.

GaN HEMT Power-up / Power-down Procedure:

To turn the transistor ON:

1. Set V_{GS} to -5 V.
2. Turn on V_{DS} to normal operation voltage (50 V).
3. Slowly increase V_{GS} to set I_{DS} current to 50 mA.
4. Apply RF power.

To turn the transistor OFF:

1. Turn the RF power off.
2. Decrease V_{GS} to -5 V.
3. Turn off V_{DS}. Wait a few seconds for Drain capacitor to discharge.
4. Turn off V_{GS}.

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Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Operating Voltage	V_{DS}		55	V
Drain to Gate Breakdown Voltage	B_{VDG}		TBD	V
Drain to Source Voltage	V_{DS}		150	V
Gate to Source Voltage	V_{GS}	-8	2	V
Drain Current	I_D		TBD	mA
RF Input Power: 50 Ω, V_{DS} = 50 V, I_{DS} = 50 mA, $T_{PKG\ BASE}$ = 85 °C.	$P_{IN\ MAX}$		TBD	dBm
RF Input Power: 10:1 VSWR, V_{DS} = 50 V, I_{DS} = 50 mA, $T_{PKG\ BASE}$ = 85 °C.	$P_{IN\ MAX}$		TBD	dBm
Output Mismatch Stress: V_{DS} = 50 V, P_{OUT} = P_{SAT} pulsed = TBD.	$VSWR_{MAX}$		TBD	
Operating Temperature (package base).	$T_{PKG\ BASE}$	-40	105	°C
Maximum Junction Temperature (MTTF > 10^6 hours).	$T_{J\ MAX}$		225	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		TBD	W

Electrostatic Discharge

Human Body Model	HBM	200		V
Charged Device Model	CDM	500		V

Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--

Note 1: Exceeding any of these limits may cause permanent damage to the device or reduce the lifetime (MTTF).
Note 2: Guerrilla-RF does not recommend sustained operation above the maximum operating conditions.

Caution! ESD Sensitive Device.
Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to *Manufacturing Note MN-001 — Package and Manufacturing Information.*

All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - *Environmental and Restricted Substance Statement Library*

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Recommended Operating Conditions

V_{DS} = 50 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
RF Frequency Range	F_{TEST}	DC	3.5	6	GHz	Typical application schematic **(notes 3 & 4)**.
Drain to Source Voltage	V_{DS}	TBD	50	TBD	V	Typical application schematic.
Drain Current (Quiescent)	I_{DQ}		50		mA	Typical application schematic.
Drain Current: RF	$I_{D\ DRIVE}$		1600		mA	Typical application schematic.
Gate Voltage	V_{GS}		-2.45		V	Typical application schematic.
Gate Current: RF	$I_{G\ DRIVE}$		TBD		mA	Typical application schematic, P_{OUT} = 50 W.
Operating Temperature	$T_{PKG\ BASE}$	-40		85	°C	On package base.
Gate Port Impedance	$Z_{RF\ IN}$		50		Ω	Typical application schematic.
Drain Port Impedance	$Z_{RF\ OUT}$		50		Ω	Typical application schematic.

V_{DS} = 28 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
RF Frequency Range	F_{TEST}	DC	3.5	6	GHz	Typical application schematic **(notes 3 & 4)**.
Drain to Source Voltage	V_{DS}	TBD	28	TBD	V	Typical application schematic.
Drain Current (Quiescent)	I_{DQ}		50		mA	Typical application schematic.
Drain Current: RF	$I_{D\ DRIVE}$		TBD		mA	Typical application schematic.
Gate Voltage	V_{GS}		-2.42		V	Typical application schematic.
Gate Current: RF	$I_{G\ DRIVE}$		TBD		mA	Typical application schematic, P_{OUT} = 50 W.
Operating Temperature	$T_{PKG\ BASE}$	-40		85	°C	On package base.
Gate Port Impedance	$Z_{RF\ IN}$		50		Ω	Typical application schematic.
Drain Port Impedance	$Z_{RF\ OUT}$		50		Ω	Typical application schematic.

Note 3: Operation outside of this range is possible but with degraded performance of some parameters.

Note 4: Contact the Guerrilla RF applications team for guidance on optimizing the tuning of the device for alternative bands.

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Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: typical application schematic using the 2.1 to 3.7 GHz tuning set, I_{DS} = 50 mA, P_{OUT} = 50 W, F_{TEST} = 3.5 GHz, Pulsed CW (100 µs pulse width, 10% duty cycle, no modulation), 50 Ω system impedance, T_{AMB} = 25 °C, $T_{PKG\ BASE}$ = -40 to 85 °C. Evaluation board losses are included within the specifications.

V_{DS} = 50 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Drain Source Breakdown Voltage	B_{VDS}	150			V	
Drain Source Leakage Current	I_{DLK}		50		µA	V_{GS} = -8 V, V_{DS} = 50 V.
Drain Current: Quiescent	I_{DQ}		50		mA	No RF applied.
Gate Threshold Voltage	V_{GS}	-3.4		-1.5	V	V_{DS} = 50 V.
Gate Bias Voltage	V_{GS}		-2.45		V	
Drain Voltage	V_{DS}		50		V	
Drain Current: RF	$I_{D\ DRIVE}$		1600		mA	RF applied, P_{OUT} = 50 W.
Gate Current: RF	$I_{G\ DRIVE}$		TBD		mA	RF applied, P_{OUT} = 50 W.
Gate Leakage Current	$I_{G\ LEAK}$		10		µA	

V_{DS} = 28 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Drain Source Breakdown Voltage	B_{VDS}	150			V	
Drain Source Leakage Current	I_{DLK}		TBD		µA	V_{GS} = -8 V, V_{DS} = 50 V.
Drain Current: Quiescent	I_{DQ}		50		mA	No RF applied.
Gate Threshold Voltage	V_{GS}	-3.4		-1.5	V	V_{DS} = 50 V.
Gate Bias Voltage	V_{GS}		-2.42		V	
Drain Voltage	V_{DS}		28		V	
Drain Current: RF	$I_{D\ DRIVE}$		TBD		mA	RF applied, P_{OUT} = 50 W.
Gate Current: RF	$I_{G\ DRIVE}$		TBD		mA	RF applied, P_{OUT} = 50 W.
Gate Leakage Current	$I_{G\ LEAK}$		10		µA	

Thermal Data

Thermal Resistance (Infrared Scan)	θ_{JC}		TBD		°C/W	P_{OUT} = TBD. On standard evaluation board.
Channel Temperature	$T_{CHANNEL}$		TBD		°C	

Note 5: MIN/MAX limits defined using modelled estimates that account for part-to-part variations and expected process spreads. As additional production lots are fabricated, accumulated test data will be used to refine the MIN/MAX limits.

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Nominal Operating Parameters – RF: DC to 6 GHz

The following conditions apply unless noted otherwise: typical application schematic using the 2.1 to 3.7 GHz tuning set, I_{DS} = 50 mA, P_{OUT} = 50 W, F_{TEST} = 3.5 GHz, Pulsed CW (100 µs pulse width, 10% duty cycle, no modulation), 50 Ω system impedance, T_{AMB} = 25 °C, $T_{PKG\ BASE}$ = -40 to 85 °C. Evaluation board losses are included within the specifications.

V_{DS} = 50 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Small Signal Gain	G_{SS}		12.8		dB	At I_{DS} ≤ 1.1* I_{DS}.
Power Gain	G_{MAX}		15.3		dB	Max gain vs. power.
Saturated Gain	G_{SAT}		12.7		dB	At 2.5 dB compression referenced to G_{MAX}.
Saturated Drain Efficiency	$D_{EFF\ SAT}$		60		%	
Saturated Output Power	P_{SAT}		46.6		dBm	
Ruggedness	Ψ	At 2.5 dB compression. Output VSWR = 10:1 (all angles)				No damage or shift in performance.

V_{DS} = 28 V

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Small Signal Gain	G_{SS}		12.1		dB	At I_{DS} ≤ 1.1* I_{DS}.
Power Gain	G_{MAX}		13.4		dB	Max gain vs. power.
Saturated Gain	G_{SAT}		10.9		dB	At 2.5 dB compression referenced to G_{MAX}.
Saturated Drain Efficiency	$D_{EFF\ SAT}$		60.4		%	
Saturated Output Power	P_{SAT}		42.3		dBm	
Ruggedness	Ψ	At 2.5 dB compression. Output VSWR = 10:1 (all angles)				No damage or shift in performance.

Note 6: MIN/MAX limits defined using *modelled estimates* that account for part-to-part variations and expected process spreads. As additional production lots are fabricated, accumulated test data will be used to refine the MIN/MAX limits.

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Typical Performance:

Single-Tone Pulse CW (10% duty cycle, 100 µs pulse width). 2nd Harmonic not optimized.

1. Optimum Peak Power at 2.5 dB compression.
2. Optimum Peak Efficiency at 2.5 dB compression.

V_{DS} = 50 V, I_{DS} = 50 mA, T_{AMB} = 25 °C

Frequency (MHz)	P_{OUT}(1) (dBm)	Gain(2) (dB)	Drain Eff(2) (%)
1000	46.7	25.3	72.4
1200	46.7	22.4	70.4
1400	46.7	20.7	68.2
1600	46.8	21.2	67.4
1800	46.9	20.8	66.3
2000	47.0	21.0	68.4
2200	47.1	20.2	67.9
2400	47.05	19.1	65.1
2600	47.08	18.8	65.4
2800	47.03	18.7	64.9
3000	46.95	18.4	65.0
3500	46.98	16.9	64.0
4000	46.9	16.7	63.7
4500	46.9	15.0	63.9
5000	46.9	14.6	62.4
5500	46.9	13.7	62.2
6000	46.9	12.8	60.7

V_{DS} = 28 V, I_{DS} = 50 mA, T_{AMB} = 25 °C

Frequency (MHz)	P_{OUT}(1) (dBm)	Gain(2) (dB)	Drain Eff(2) (%)
1000	44.1	22.1	71.3
1200	44.1	21.5	69.2
1400	44.0	20.3	66.5
1600	44.2	20.1	65.9
1800	44.4	19.2	66.3
2000	44.5	18.5	66.7
2200	44.5	17.5	66.6
2400	44.5	17.8	65.5
2600	44.6	17.1	66.0
2800	44.5	16.4	66.0
3000	44.6	16.5	66.9
3500	44.5	15.1	65.7
4000	44.6	14.9	66.4
4500	44.5	13.7	66.8
5000	44.4	12.7	64.2
5500	44.4	11.7	63.6
6000	44.4	10.8	63.3

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Typical Performance:

Load Pull Measurements: V_{DS} = 50 V, I_{DS} = 47 mA. Single-Tone Pulse CW (10% duty cycle, 100 µs pulse width).

For Optimum Peak Power at 2.5 dB Compression

Frequency (MHz)	Z_{IN} (F0)	ZI (F0)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	Eff (%)	AMPM (deg)
1000	3.3 -j12.8	18.4 +j3.7	24.1	46.7	46.77	62.4	4.1
1200	2.8 -j10.8	20.3 +j3.3	22.6	46.7	46.77	60.6	3.6
1400	3.0 -j9.2	20.0 +j3.0	21.0	46.7	46.77	58.9	3.3
1600	2.9 -j7.7	16.8 +j6.5	20.7	46.8	47.86	59.1	5.3
1800	2.7 -j7.0	16.4 +j6.0	19.6	46.9	48.98	58.2	3.7
2000	2.4 -j5.6	13.9 +j5.0	19.3	47.0	50.12	55.2	3.5
2200	1.6 -j3.9	14 +j6.2	18.8	47.10	51.29	58.8	1.0
2400	1.7 -j2.9	12.3 +j5.2	18.4	47.05	50.7	55.9	2.4
2600	1.4 -j1.9	12.2 +j5.3	18.0	47.08	51.05	56.0	2.0
2800	1.5 -j1.6	11.6 +j6.9	18.0	47.03	50.47	58.5	2.1
3000	1.4 -j1.3	11.6 +j7.0	17.4	46.95	49.55	58.6	1.6
3500	1.6 +j0.2	9.8 +j4.9	15.8	46.98	49.89	56.0	2.6
4000	1.0 +j2.3	8.3 +j3.9	15.2	46.9	48.98	55.6	0.4
4500	1.3 +j3.1	6.2 +j3.1	14.3	46.9	48.98	56.2	0.7
5000	1.4 +j4.3	6.1 +j2	13.5	46.9	48.98	55.0	1.6
5500	1.2 +j5.2	5.9 +j1.5	12.5	46.9	48.98	56.0	1.7
6000	1.6 +j6.5	5.1 +j0.0	11.9	46.9	48.98	56.2	2.1

For Optimum Peak Efficiency at 2.5 dB Compression

Frequency (MHz)	Z_{IN} (F0)	ZI (F0)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	Eff (%)	AMPM (deg)
1000	2.4 -j10.3	31.0 +j26.3	25.3	44.4	27.54	72.4	0.1
1200	2.5 -j9.8	30.5 +j10.4	22.4	46.1	40.74	70.4	4.1
1400	2.5 -j7.7	36.2 +j21.0	20.7	44.9	30.9	68.2	3.0
1600	2.0 -j6.7	18.9 +j19.0	21.2	45.6	36.31	67.4	1.9
1800	2.1 -j5.3	15.9 +j18.6	20.8	45.1	32.36	66.3	1.6
2000	1.4 -j4.1	12.8 +j18.2	21.0	45.3	33.88	68.4	1.4
2200	1.4 -j2.9	11.9 +j17.1	20.2	45.3	33.88	67.9	2.1
2400	1.3 -j2.0	13.2 +j13.8	19.1	46.0	39.81	65.1	1.9
2600	1.1 -j1.3	11.2 +j15.1	18.8	45.7	37.15	65.4	2.0
2800	1.0 -j0.9	10.3 +j13.4	18.7	46.0	39.81	64.9	1.3
3000	1.0 -j0.5	9.3 +j12.0	18.4	46.2	41.69	65.0	0.1
3500	1.2 +j1.0	6.9 +j10.7	16.9	45.6	36.31	64.0	1.3
4000	0.9 +j2.6	5.0 +j7.2	16.7	45.9	38.90	63.7	-0.2
4500	1.1 +j3.5	4.3 +j6.7	15.0	45.6	36.31	63.9	0.8
5000	1.0 +j4.9	4.1 +j4.5	14.6	45.9	38.90	62.4	0.6
5500	1.0 +j5.4	2.9 +j3.2	13.7	45.3	33.88	62.2	-0.8
6000	1.2 +j6.9	2.6 +j1.8	12.8	45.0	31.62	60.7	0.3

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Typical Performance:

Load Pull Measurements: V_{DS} = 28 V, I_{DS} = 47 mA. Single-Tone Pulse CW (10% duty cycle, 100 µs pulse width).

For Optimum Peak Power at 2.5 dB Compression

Frequency (MHz)	Z_{IN} (F0)	ZI (F0)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	Eff (%)	AMPM (deg)
1000	3.3 -j13.0	11.5 -j0.7	21.45	44.1	25.7	60.2	3.8
1200	2.5 -j10.4	10.1 -j1.3	20.2	44.1	25.7	55.1	2.0
1400	2.7 -j8.9	11.7 -j1.5	18.7	44.0	25.12	57.2	1.0
1600	2.3 -j7.7	12.5 +j0.8	18.5	44.2	26.3	60.2	3.7
1800	2.3 -j6.8	10.6 +j1.0	18.2	44.4	27.54	58.1	3.3
2000	2.1 -j5.3	10.4 +j1.0	17.7	44.5	28.18	58.4	3.8
2200	1.6 -j3.8	10.1 +j0.8	17.1	44.5	28.18	58.9	1.5
2400	1.5 -j2.7	10.0 +j0.6	16.5	44.5	28.18	58.1	1.9
2600	1.4 -j2.0	10.0 +j0.6	16.1	44.6	28.84	58.7	1.0
2800	1.1 -j1.5	10.0 +j0.6	15.7	44.5	28.18	58.0	0.6
3000	1.1 -j1.1	10.0 +j0.7	15.3	44.6	28.84	58.1	0.1
3500	1.3 +j0.4	9.2 +j0.3	14.0	44.5	28.18	58.1	-0.3
4000	1.1 +j2.3	7.4 +j0.3	13.8	44.6	28.84	59.7	0.1
4500	1.3 +j3.1	7.0 -j0.6	12.3	44.5	28.18	57.1	-1.3
5000	1.2 +j4.7	6.8 -j1.4	11.6	44.4	27.54	56.3	-0.3
5500	1.3 +j5.2	5.7 -j2.0	10.9	44.4	27.54	57.3	0.5
6000	1.4 +j6.8	6.5 -j3.7	9.8	44.4	27.54	54.7	0.0

For Optimum Peak Efficiency at 2.5 dB Compression

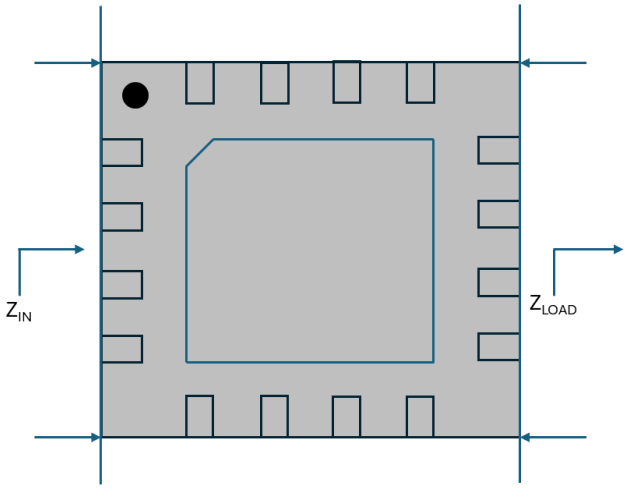
Frequency (MHz)	Z_{IN} (F0)	ZI (F0)	Gain (dB)	P_{OUT} (dBm)	P_{OUT} (W)	Eff (%)	AMPM (deg)
1000	2.4 -j10.7	22.4 +j6.5	22.1	42.4	17.38	71.3	2.0
1200	2.0 -j8.5	23.4 +j11.5	21.5	41.7	14.79	69.2	2.3
1400	2.1 -j7.8	20.9 +j9.3	20.3	42.2	16.6	66.5	1.1
1600	1.8 -j6.5	14.6 +j8.3	20.1	43.0	19.95	65.9	0.9
1800	1.7 -j5.4	14.0 +j9.1	19.2	43.0	19.95	66.3	0.8
2000	1.6 -j4.2	13.4 +j12.5	18.5	42.1	16.22	66.7	0.9
2200	1.4 -j2.7	13.6 +j7.8	17.5	43.3	21.38	66.6	0.3
2400	1.1 -j1.8	10.9 +j8.2	17.8	43.0	19.95	65.5	-0.1
2600	1.1 -j1.1	10.8 +j8.2	17.1	43.1	20.42	66.0	0.1
2800	1.1 -j0.8	11.0 +j8.2	16.4	43.2	20.89	66.0	1.1
3000	1.1 -j0.5	9.5 +j7.2	16.5	43.4	21.88	66.9	-0.1
3500	1.1 +j1.2	7.7 +j5.5	15.1	43.4	21.88	65.7	-1.7
4000	0.9 +j2.8	5.3 +j4.4	14.9	43.0	19.95	66.4	-2.9
4500	1.0 +j3.7	4.2 +j3.3	13.7	42.8	19.05	66.8	-3.8
5000	1.0 +j5.1	4.4 +j2.0	12.7	43.0	19.95	64.2	-2.6
5500	1.0 +j5.6	3.9 +j0.2	11.7	43.1	20.42	63.6	-3.0
6000	1.2 +j7.1	4.0 -j1.0	10.8	43.4	21.88	63.3	-1.7

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Load Pull Measurement Notes:

1. Source and load impedance at 2nd harmonic are set to 10 Ω.
2. With proper 2nd harmonic termination, expect +5% efficiency for Source and similar with Drain 2nd harmonic.
3. Z_{LOAD}: measured impedance presented to the output of the device in the reference plane.
4. Z_{IN}: measured input impedance at the input of the device in the reference plane.

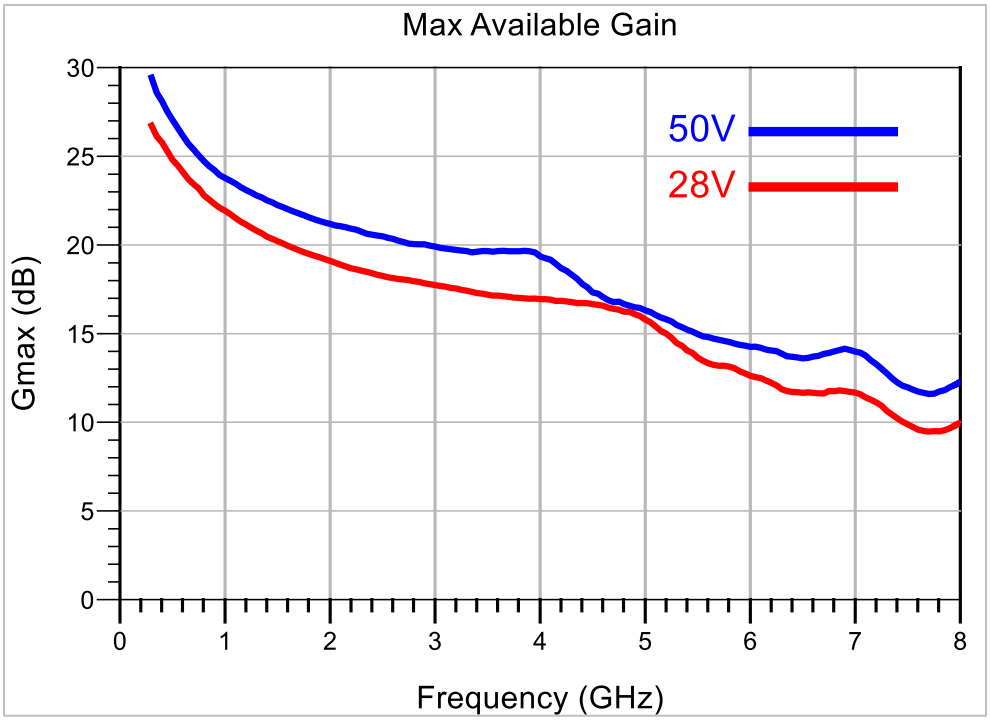
Impedance Reference Plane

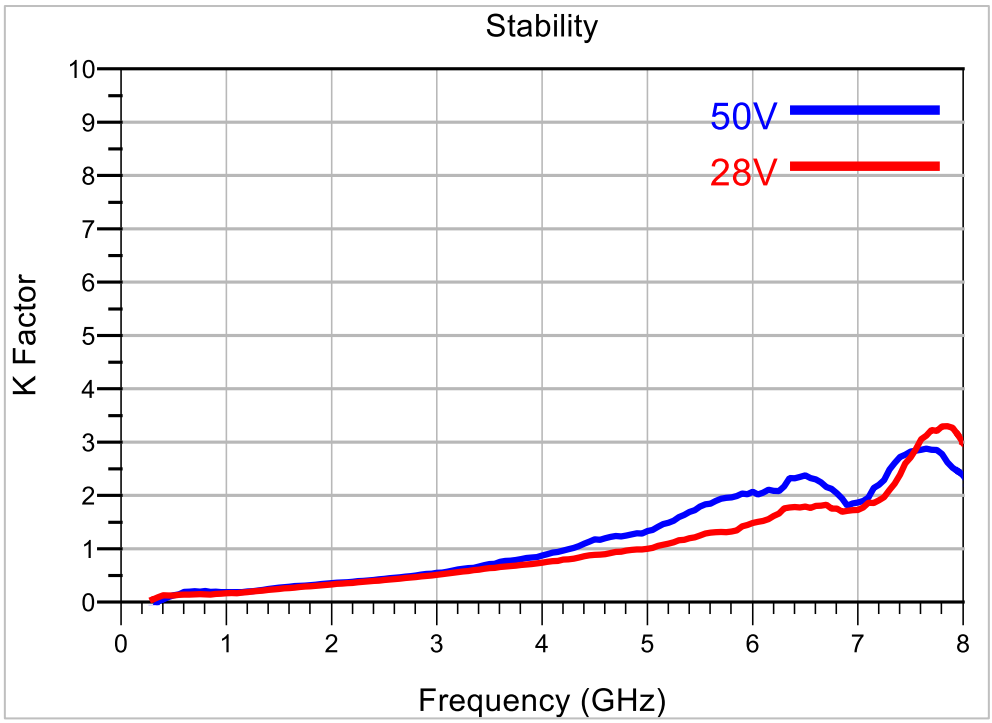


Raw data and full Load Pull measurement report available at request: sales@guerrilla-rf.com

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Typical Operating Curves: Maximum Available Gain and K-Factor vs. Frequency



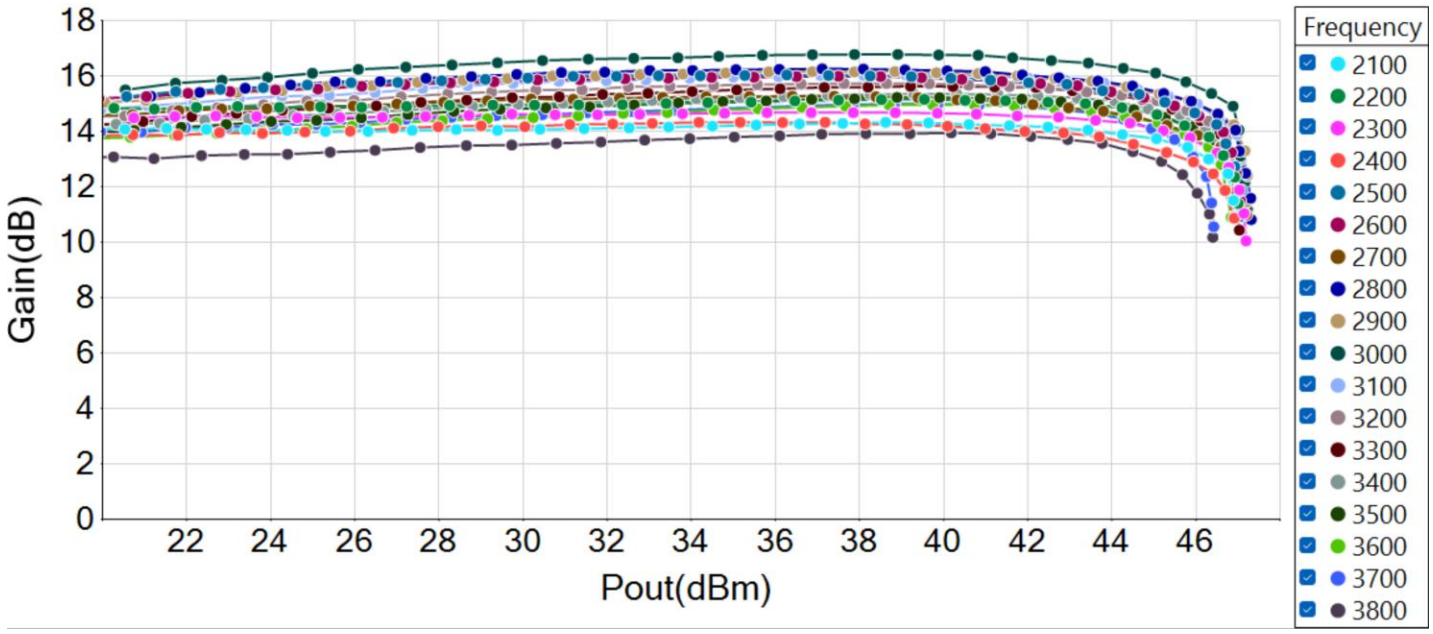


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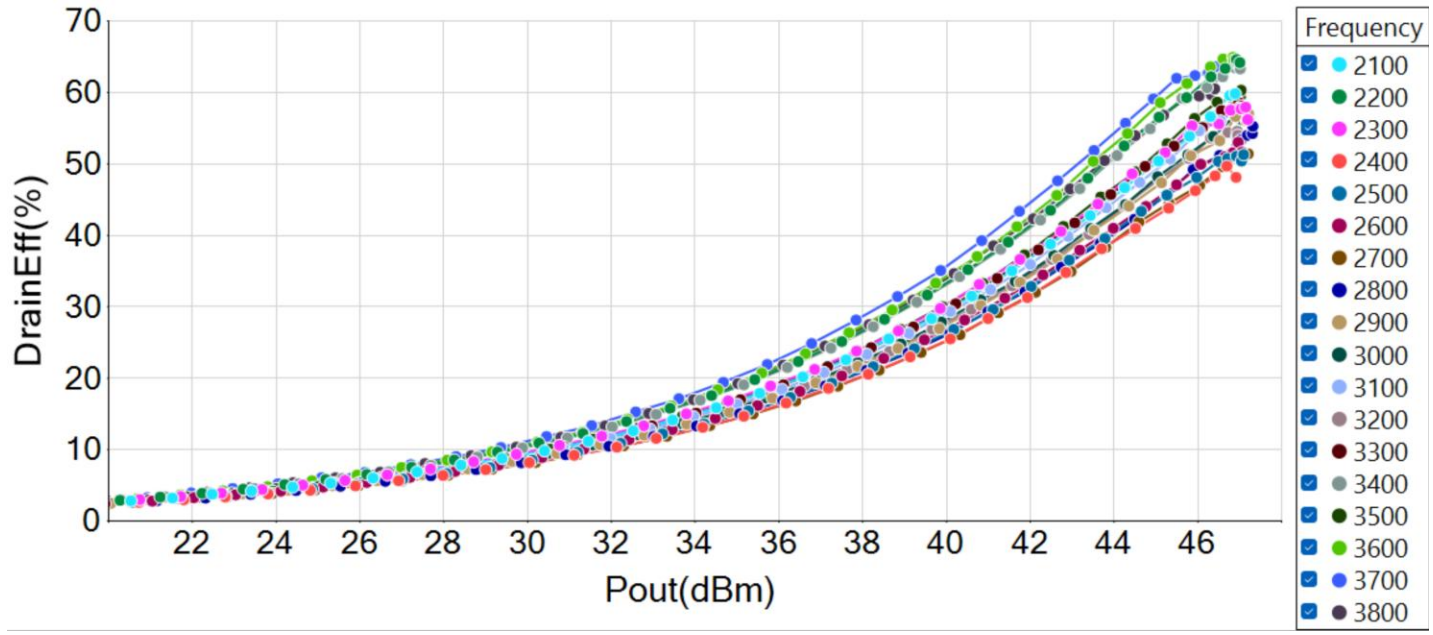
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Typical Operating Curves: 2.1 to 3.8 GHz, 50 V, Pulsed CW (10% duty cycle, 100 µs pulse width)

Gain vs. Output Power and Frequency



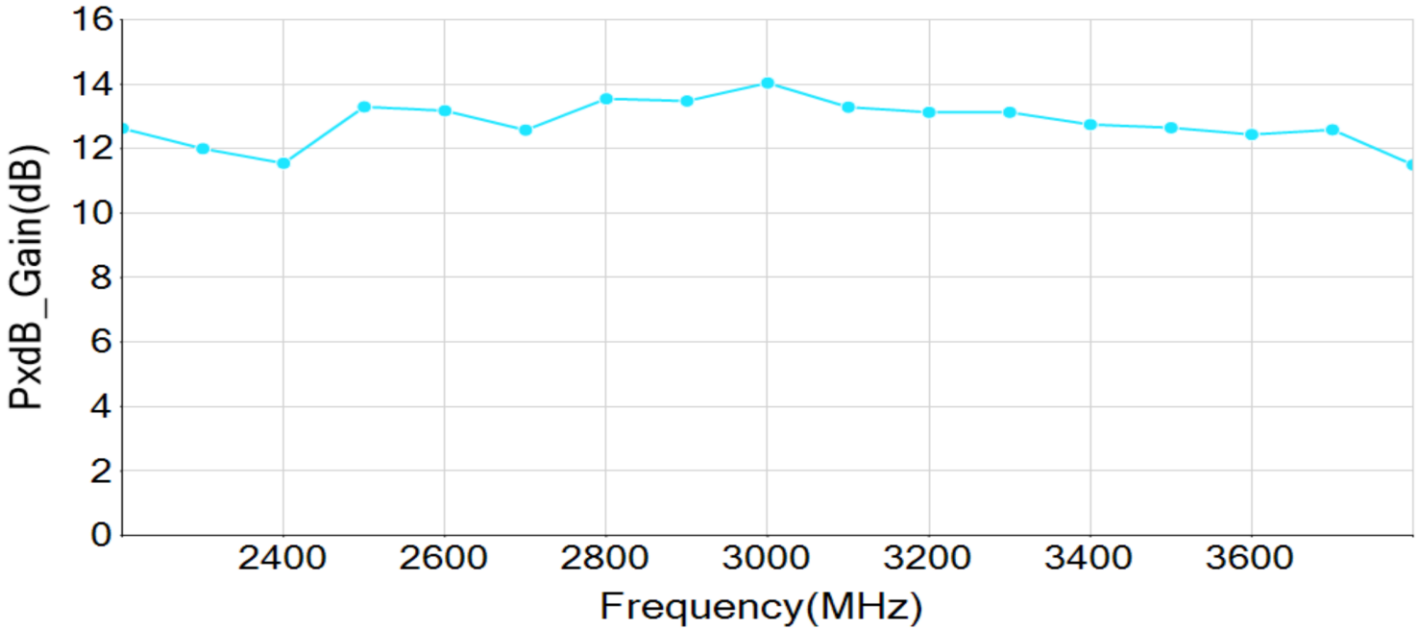
Drain Efficiency vs. Output Power and Frequency



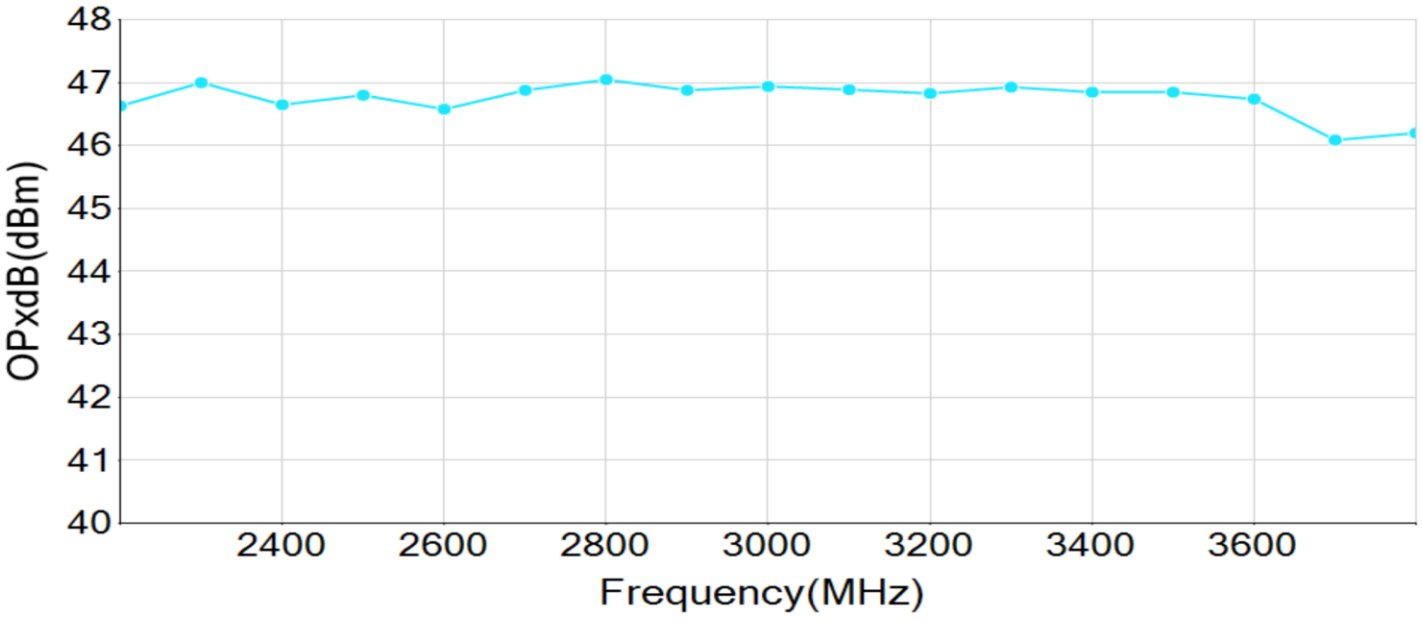
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Typical Operating Curves: 2.1 to 3.8 GHz, 50 V, Pulsed CW (10% duty cycle, 100 µs pulse width)

Gain vs. Frequency: 2.5 dB Compression



P_{OUT} vs. Frequency: 2.5 dB Compression

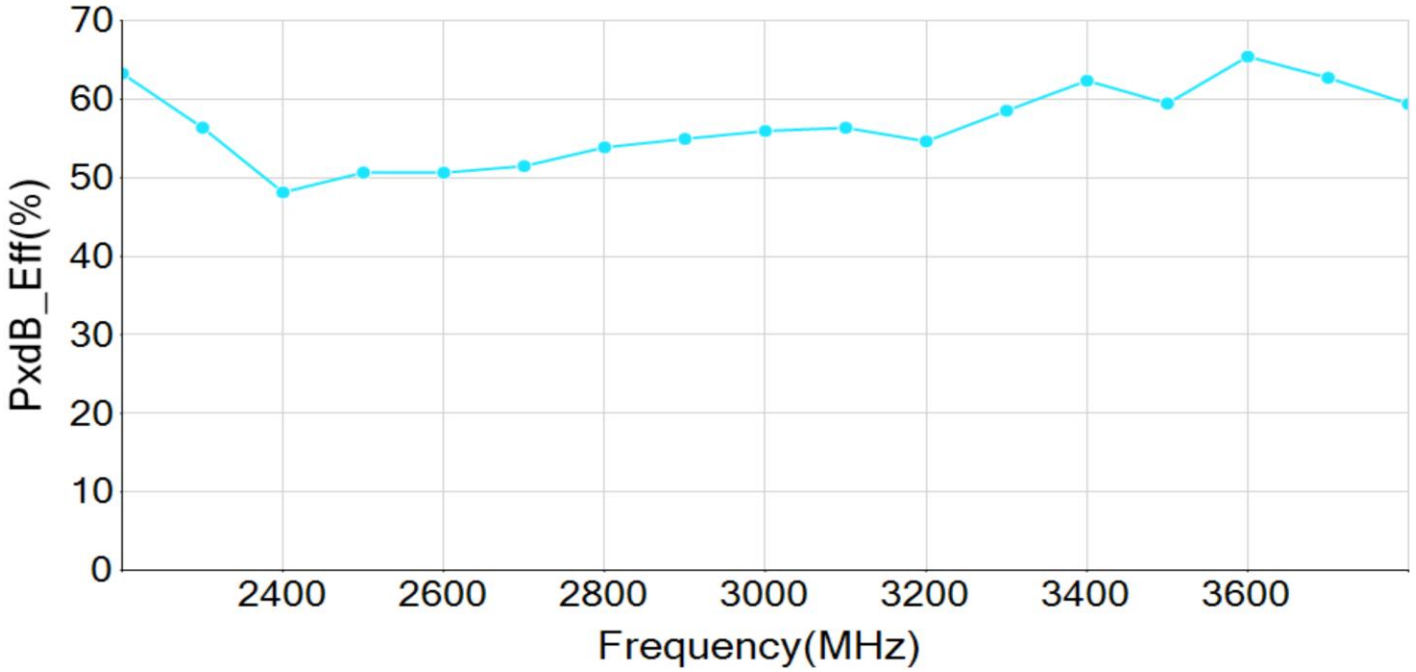


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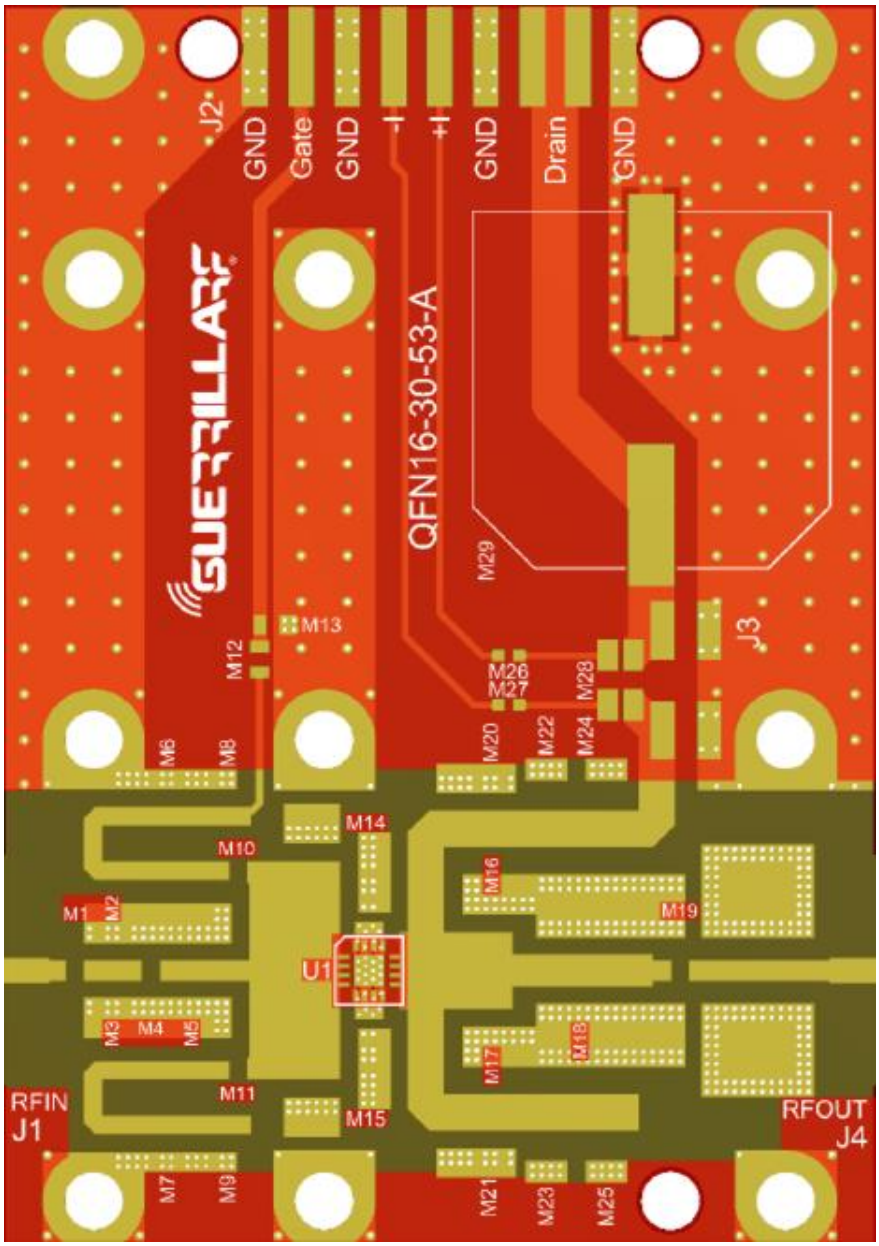
Typical Operating Curves: 2.1 to 3.8 GHz

Drain Efficiency vs. Frequency: 2.5 dB Compression



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GRF0030 Evaluation Board Assembly Diagram



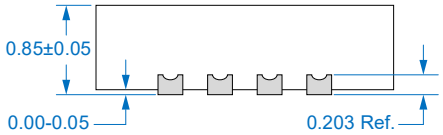
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GRF0030 Evaluation Board Assembly Diagram Reference: 2.1 to 3.8 GHz Tune

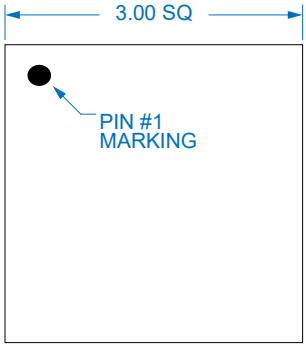
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Resistor	Various	5%	2 Ω	0805	ok
M2, M3	Resistor	Various	5%	1 kΩ	0805	ok
M4	Capacitor	ATC	N/A	3.3 pF	0805	ok (Murata)
M5	Capacitor	ATC	N/A	1.1 pF	0805	ok (Murata)
M6	Capacitor	ATC	N/A	6.8 pF	0805	ok (Murata)
M7, M9, M11	DNP	--	--	--	--	--
M8	Capacitor	Murata	GRM	10 nF	0805	ok
M10	Resistor	Various	5%	5.1 Ω	0805	ok
M12	Resistor	Various	5%	0 Ω	0805	ok
M13	Capacitor	Murata	GRM	10 µF	0805	ok
M14, M15	Capacitor	ATC	N/A	0.7 pF	0805	ok (Murata)
M16, M17	Capacitor	ATC	N/A	0.6 pF	0805	ok (Murata)
M18	Capacitor	ATC	N/A	0.8 pF	0805	ok (Murata)
M19	Capacitor	ATC	N/A	7.5 pF	0805	ok (Murata)
M20, M21	Capacitor	ATC	N/A	6.8 pF	0805	ok (Murata)
M22, M23, M24, M25	Capacitor	Murata	GRM	10 µF	1210	ok
M26, M27	Resistor	Various	5%	1 kΩ	0603	ok
M28	Resistor	Ohmite	5%	0.01 Ω	1206	ok
M29	Electrolytic Capacitor	Various	N/A	220 µF	12.5 mm	ok
U1	GaN PA GRF0030	Guerrilla-RF	--	--	3 x 3 mm	no
J1, J4	SMA Connector	Various	--	--	--	--
J2	DC Connector	Various	--	--	--	--
J3	DNP	--	--	--	--	--
Evaluation Board	QFN16-30-53-A	--	--	--	--	--

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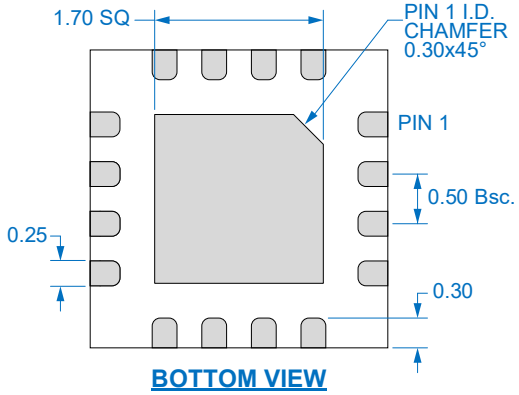
Package Outline Drawing



SIDE VIEW

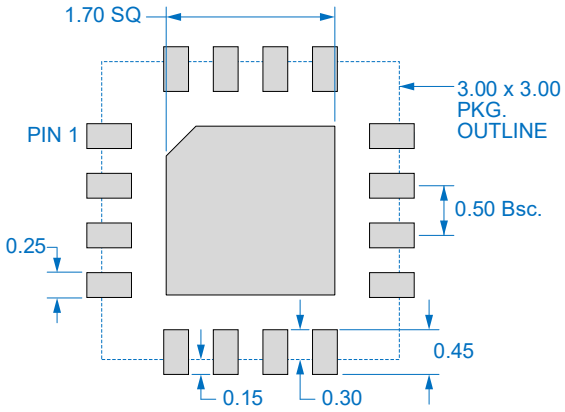


TOP VIEW



BOTTOM VIEW

Recommended PCB Footprint



TOP VIEW

3 x 3 mm QFN-16 Package Dimensions

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Package Marking Diagram

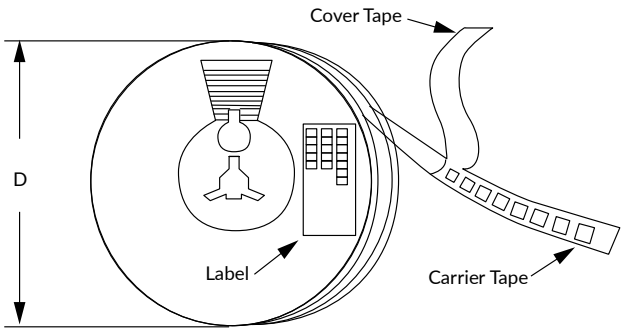


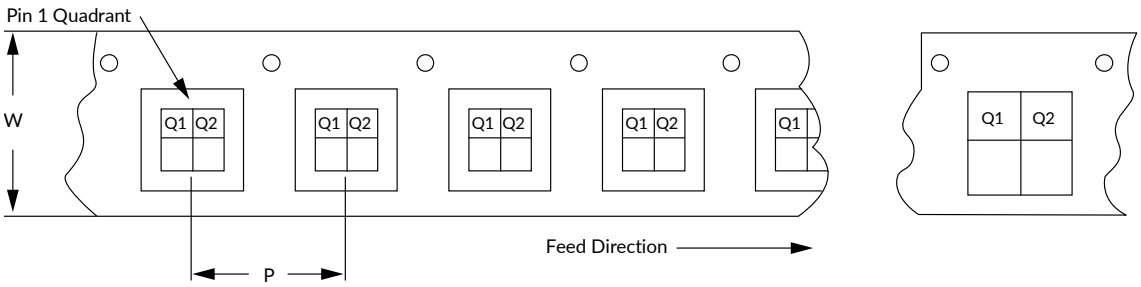
- Line 1: "YY" = YEAR. "WW" = WORK WEEK the device was assembled.
- Line 2: "GRF" = Guerrilla RF.
- Line 3: "XXXX" = Device PART NUMBER.

Tape and Reel Information

Guerrilla RF's tape and reel specification complies with Electronic Industries Alliance (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.
For the Tape and Reel Reference Table, please refer to: https://www.guerrilla-rf.com/prodFiles/Manufacturing/MN001.pdf





Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

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Revision History

Revision Date	Description of Change
September 23, 2024	Preliminary Data Sheet.
October 23, 2024	Updated plots on pages 10, 11 & 12.
May 1, 2025	Converted Data Sheet to new format only. No change to device or device specifications.
May 27, 2025	Updated Tables on page 8, 9 & 10. Added new Max Gain and Stability plots. Removed S-parameter plots.
August 11, 2025	Release Ø Data Sheet. Added 28 volt specifications.

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Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-Parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-Parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with *production-released* material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using *modelled estimates* that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material *derived from multiple lots which have been fabricated over an extended period of time*. MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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