GMV3347-GM1-STD

Monday, June 9, 2025 11:47 AM

TEST NOTES
Test Vb, Ct, Vf and Ir 100%. All other parameters taken from wafer evaluation or based on Lot approval.
** Wafer Probe CJ4V limits.

REF. SPECS
N/A

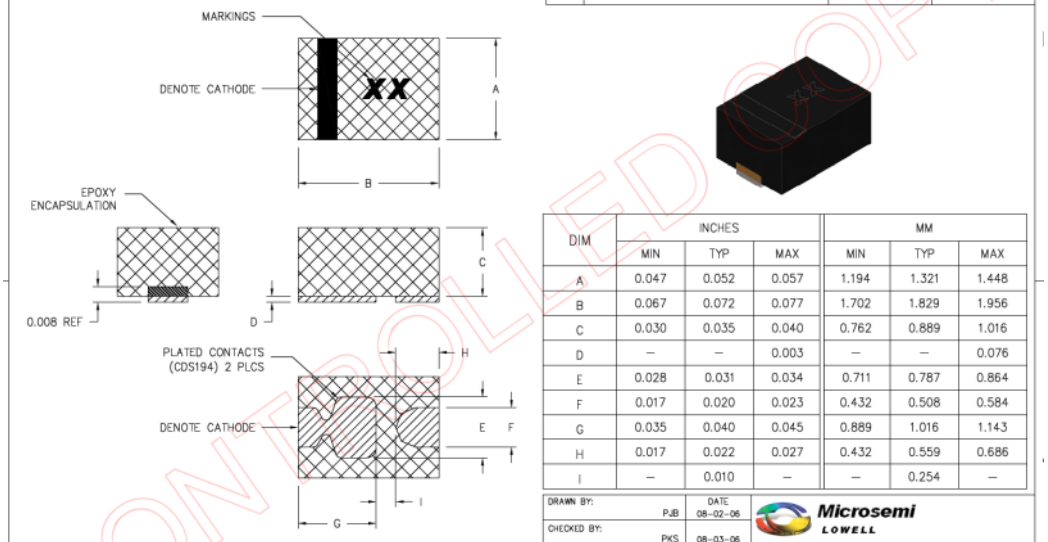
TEST	UNITS	CONDITIONS	TEMP	CUSTOMER LIMITS			Factory LIMITS		
				Min	Typ	Max	Min	Typ	Max
Vb	V	Ir = 10 uA	25C	22.0					
Ct	pF	f = 1 MHz, Vr = 4 V	25C	3.6		4.4			
Ir	nA	Vr = 18 V	25C			20.0			
Q		f = 50 MHz, Vr = 4 V	25C	3000.0					
Gamma		2 to 20 V, Note 1	25C		1.0				
Cj **	pF	f = 1 MHz, Vr = 4V	25C				3.5		4.2
Vf	V	If = 10 mA	25C						1.4

ADDITIONAL NOTES
1. Gamma value is guaranteed for unpackaged chips only.

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REVISION

REV	DESCRIPTION	DATE	APPROVED BY
B	REVISED PER SAO #12361	09-24-08	PJB
C	REVISED PER SAO #17220	05-21-12	PJB



DIM	INCHES			MM		
	MIN	TYP	MAX	MIN	TYP	MAX
A	0.047	0.052	0.057	1.194	1.321	1.448
B	0.067	0.072	0.077	1.702	1.829	1.956
C	0.030	0.035	0.040	0.762	0.889	1.016
D	–	–	0.003	–	–	0.076
E	0.028	0.031	0.034	0.711	0.787	0.864
F	0.017	0.020	0.023	0.432	0.508	0.584
G	0.035	0.040	0.045	0.889	1.016	1.143
H	0.017	0.022	0.027	0.432	0.559	0.686
I	–	0.010	–	–	0.254	–

DRAWN BY: PJB	DATE 08-02-06	Microsemi LOWELL
CHECKED BY: PKS	08-03-06	
ENG DJD	08-07-06	GIGAMITE PACKAGE OUTLINE
PROD CP	08-07-06	
QA DB	08-07-06	SIZE A / CAGE CODE 50101 / DRAWING NO. 743-GM1
THIRD ANGLE PROJECTION		DO NOT SCALE DRAWING / SHEET 1 OF 1

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON FINISH: RMS 63
ANGLES: ±½ DEG
FRACTION: ±1/64
4 PLACE DECIMALS: ±.0005
3 PLACE DECIMALS: ±.005
2 PLACE DECIMALS: ±.02

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